



INFORMATION DISCLOSURE CITATION

OMB No. 0651-0011

Atty. Docket No.	06502.0128-01	Appln. No.	067,349/10
Applicant	Gilad BRACHA et al.		
Filing Date	February 7, 2002	Group:	217 2145

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Filing Date If Appropriate
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	6223346B1	April 24, 2001	Tock	
	6061721	May 09, 2000	Ismael et al.	

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Document Number	Publication Date	Country	Translation Yes or No
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	J. Stanley Warford, <i>BlackBox: A New Object-Oriented Framework for CS1/CS2</i> , ACM Press (1998)
	"java.lang: Class ClassLoader", visited at http://www.javasoft.com/products/j...ocs/api/java/lang/ClassLoader.html on 10/15/99, 14 pages
	"Oberon Microsystems: Component Software Resources", visited at http://www.oberon.ch/resources/index.html#Component Pascal on 10/15/99, 5 pages
	"Oberon Microsystems: The Evolution of Oberon-2 to Component Pascal", visited at http://www.oberon.ch/resources/component_pascal/evolution.html on 10/15/99, 12 pages
	"Oberon Microsystems: Component Pascal Language Report", visited at http://www.oberon.ch/resources/component_pascal/language_report.html on 10/15/99, 29 pages
	"Oberon Microsystems: Brief Comparison of Component Pascal and Java", visited at http://www.oberon.ch/resources/component_pascal/java_component_pascal.html on 10/15/99, 3 pages

Examiner	DIANE MIZRAHI PRIMARY EXAMINER	Date Considered	11/8/05
*Examiner:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		
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INFORMATION DISCLOSURE CITATION
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Atty. Docket No.: 06502.0128-01000	Application No.: 067,349/10
Applicant: Gilad BRACHA et al.	
Filing Date: February 7, 2002	Group 2171 2465

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ll	5,504,568	4/2/1996	Saraswat et al.			
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	Cardelli, Luca, "Program Fragments, Linking, And Modularization," Conference Record of POPL: ACM Sigplan-Sigact Symposium on Principles of Programming Language, US, New York, ACE, Conf. 28, 15 January 1997, pages 266-277, XP000741821
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	Fong et al., Phillip W.L., "Proof Linking: An Architecture for Modular Verification of Dynamically-Linked Mobile Code," Software Engineering Notes, US, Association for Computing Machinery, New York, Vol. 23, No. 6, Nov. 1998, pages 220-230, XP000831185
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Group ~~217~~ 2165**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)** Litvinov, Vassily, "Constraint-Based Polymorphism in Cecil: Towards a Practical and Static Type System," Department of Computer Science and Engineering, University of Washington, XP-000827881, October 1998, pp. 388-411

Examiner

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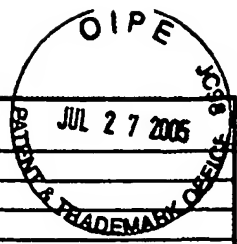
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IDS Form PTO/SB/08: Substitute for form 1449A/PTO			Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Application Number	10/067,349	
			Filing Date	February 7, 2002	
			First Named Inventor	Gilad BRACHA	
			Art Unit	2111 2/6	
			Examiner Name	Unknown	
Sheet	1	of	1	Attorney Docket Number	06502.0128-01000



U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
Examiner Initials	Cite No.	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code (if known)			
		6,601,114 B1	7/29/2003	Bracha et al.	
		6,618,769 B1	9/9/2003	Bracha et al.	
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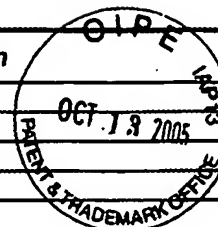
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		Country Code Number Kind Code (if known)				
		JP 8-022393	1/23/1996	Kosaku		Yes
		JP 9-152961	6/10/1997	Evans et al.		Yes

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation
		Goldberg, A., "A Specification of Java Loading and Bytecode Verification", Proceedings of 5th ACM Conference on Computer and Communications Security, San Francisco, CA, November 1998, pp. 49-58	
		Jensen, T., et al., "Security and Dynamic Class Loading in Java: A Formalization", Proceedings of the 1998 IEEE Intl. Conference on Computer Languages, Chicago, IL, May 16, 1998, pp. 4-15	
		Jensen, T., et al., "Verification of Control Flow Based Security Properties", Proceedings of the 1999 IEEE Symposium on Security and Privacy, May 12, 1999, pp. 89-103	

Examiner Signature		Date Considered	11/8/05
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			Filing Date	February 7, 2002	
			First Named Inventor	Gilad BRACHA	
			Art Unit	2171	
			Examiner Name	Unknown	
Sheet	1	of	1	Attorney Docket Number	06502.0128-01000



U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS					
Examiner Initials	Cite No.	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code (if known)			
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Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation
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NON PATENT LITERATURE DOCUMENTS			
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